

		Job Description			
Job Title:	Electrical Engineer (Modelling, Verification and Integration)				
Company:	Schindler	Work location:	Shanghai / Jiading		
Department:	R&D-AP	Reporting to position:			
Management grade:	Junior Engineer	Signatory right:	Campus		
Number of direct subordinates:	none	Number of indirect subordinates:	none		
Direct budget responsibility:	none	Indirect budget responsibility:	Project budgets		
Objectives of the function:	<ul style="list-style-type: none"> • The job holder fulfills his/her mission within the R&D-CO / AP electrical department . • He/She is responsible for development of hardware in loop test program and test bench. 				
Main tasks:	<p>Test hardware and software Development</p> <ul style="list-style-type: none"> -Development of test software with labview or other program language; -Develop the hardware in loop test bench with NI and other related elevator hardware; - Define the test software and release test - Support to write test case for automatic bench tester <p>Engineering module build</p> <ul style="list-style-type: none"> Build the elevator module with Matlab; Cooperate with foreign partner to release engineering Module; 				
Education:	<ul style="list-style-type: none"> • Major with Automation control, computer science and technology, motor control; Power Electronics, Electronical and Electronics; • Bachelor or Master degree; 				
Languages:	<ul style="list-style-type: none"> • Good English 				
IT:	Excellent knowledge of office software				
Professional skills:	<ul style="list-style-type: none"> • Engineering and design methods • Quality and reliability engineering methods 				
Preferences:					